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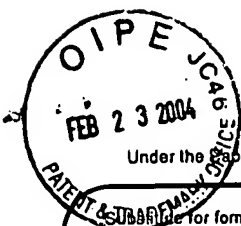
Sheet 1 of 2

Application Number	10/614,423
Filing Date	July 7, 2003
First Named Inventor	Shrimali
Art Unit	unknown
Examiner Name	unknown
Attorney Docket Number	RAMB-01013US0

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 2 of 2**Complete If Known**

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/S.K./		DAI & PRABHAKAR, "The throughput of data switches with and without speedup"	
/S.K./		BENETT & ZHANG, "Hierarchical Packet Fair Queueing Algorithms"	
/S.K./		ZHANG, TOWSLEY & KUROSE, "Statistical Analysis of the Generalized Processor Sharing Scheduling Discipline"	
/S.K./		SHREEDHAR & VARGHESE, "Efficient Fair Queueing using Deficit Round Robin", November 18, 1994	
/S.K./		CHI & TAMIR, "Decomposed Arbiters for Large Crossbars with Multi-Queue Input Buffers", Proceedings of the International Conference on Computer Design, Cambridge, MA, pp. 233-238, October, 1991.	

Examiner
Signature

/Suk Jin Kang/

Date
Considered

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